
FLORENCIO MOSTRALES APOLINAR JR

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Summary

Twenty six (26) years experience in the semiconductor and electronics manufacturing industry particularly in the project management of product and test development, product qualification, production start-up, process certification and volume ramp.

Outstanding coordination and communication competence proven with the successful leadership of cross-functional teams.

Recognized expert in TRIZ knowledge-based systematic innovation methodology with combined 12 years of practical experience and personal research.

Proficient in several computer programming languages. Applied C, C++ under Unix, and SunOS for test programs. Web development using HTML/CSS and LAMP (Linux, Apache, MySQL and PHP). Engineering education using Scilab and Python.

Professional Experience

Ateneo De Manila University, Loyola Heights, Quezon City <i>Career Highlights</i>	Lecturer	2006 – present
	Significant contribution in the study and development of factory energy management and efficiency models for heat exchangers under the research grant provided by Hitachi Global Storage Technologies.	
ADDMorE Business Solutions Inc., West Kamias, Quezon City <i>Career Highlights</i>	President	2009 – present
	Successful development and deployment of several workshops as well as in-company training.	
	Provided lectures on productivity and innovation to academe (UP Diliman ISSI, UP Los Banos, ACSAT, TIP, Ateneo), professional organizations (PMAP, IECEP, Asemep) and companies (IMI, PSI, Toshiba, EMS, Pricon)	
Intel Technology Philippines Manufacturing Inc, General Trias, Cavite <i>Career Highlights</i>	Engineering Manager	1994 – 2006
	Successful transfer, start-up and production ramp into test manufacturing of Intel's Pentium microprocessor and Chipsets products.	
	Development and deployment of massively parallel testing technology for	

Intel's Flash products.

Study and research of Micro-electromechanical Systems (MEMS) which lead to the development of MEMS switch actuator patents.

Intel Philippines Manufacturing Inc., Makati, Metro Manila	Section Head	1984 – 1994
<i>Career Highlights</i>	Development and implementation of smart quick pulse algorithm the greatly enhance Intel's proprietary programmers for EPROM products.	
	Study of memory cell row disturb device reliability phenomena which eventually led to the implementation of an innovative program algorithm at Sort test.	
	Initiated the foundations for establishing product design capability at Intel Philippines' site.	
Philips Components Philippines Inc. Las Pinas, Metro Manila	Senior Test Engineer	1983-1984
<i>Career Highlights</i>	Failure analysis of signal and audio transistor products and the development of tester algorithms for screen electrical failures.	
Signetics Filipinas Inc. Barrio Ugong, Pasig	Senior Test Engineer	1981 – 1983
<i>Career Highlights</i>	Start-up and deployment of advanced electrical failure analysis capability at Signetics with the installation of fault isolation test station, electrical micro-probing and de-layering chemistries.	
Semiconductor Devices Philippines Inc. FTI Taguig, Metro Manila	QC/QA Supervisor	1979 – 1981
<i>Career Highlights</i>	Installation of a creative incoming inspection process to reduce defective piece parts being installed in expensive electronics modules.	
Quiogue Electrical Engineering and Associates, Cubao, Quezon City	Draftsman	1975 – 1976
<i>Career Highlights</i>	Electrical drawing specifications for building and residential houses including Manila Midtown Hotel and Rio Tuba school building.	

Education

Master of Science in Electronics and Communications Engineering (9 units)

De La Salle University, Taft, Metro Manila

Bachelor of Science in Electronics and Communications Engineering (Graduate)

University of Santo Tomas, Espana, Metro Manila

Electrical Technician Course (Graduate)

Marikina School of Arts and Trades, Marikina
